



HPC – The Next 5 Years

Towards a Fusion™ Accelerated Processing Unit (APU)

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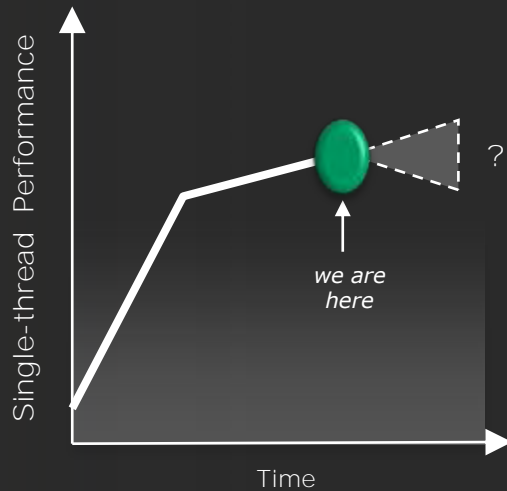


A New Era of Processor Performance

Single-Core Era

Constrained by:

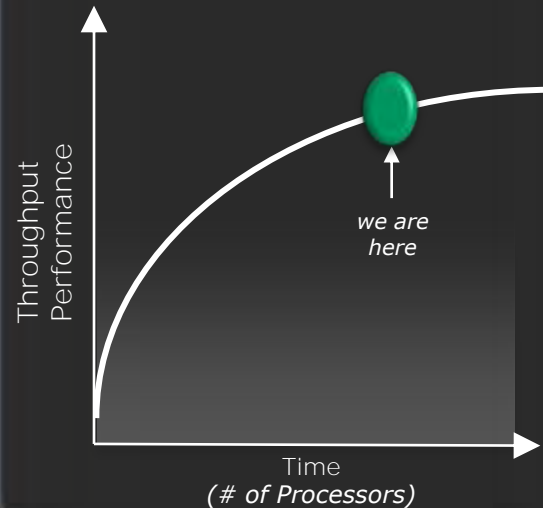
- ✗ Power
- ✗ Complexity



Multi-Core Era

Constrained by:

- ✗ Power
- ✗ Parallel SW availability
- ✗ Scalability



Direct Connect Architecture 1.0

Balanced and Scalable Design to Support up to 6 Cores



✓ No front side bus

✓ Integrated memory controller

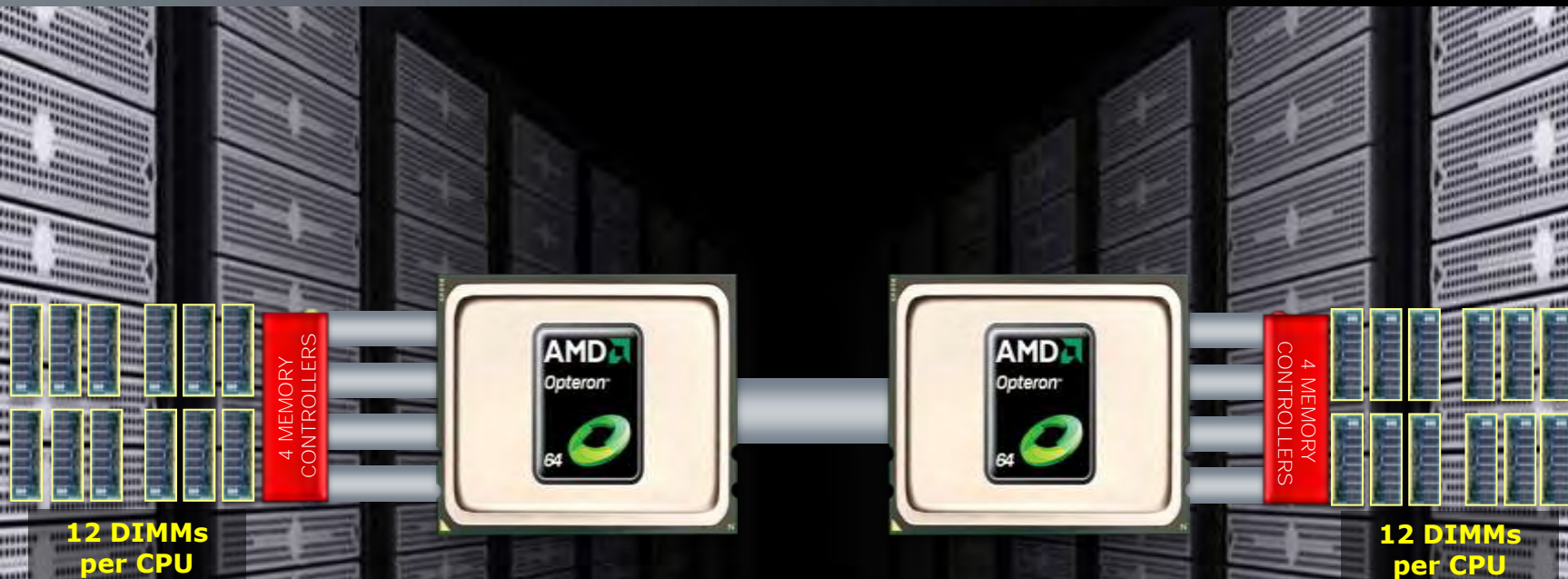
✓ HyperTransport™ technology

✓ NUMA memory architecture



Direct Connect Architecture 2.0

Balanced and Scalable Design to Support up to 16 Cores* per CPU



✓ 1-hop between processors

✓ Four memory controllers

✓ Up to 50% more DIMMs

✓ Up to 33% increase in CPU to CPU communication speed**

*16-core configuration planned for upcoming AMD processor core codenamed "Bulldozer."

**Based on HyperTransport™ technology links @ up to 4.8 GT/s for Six-Core AMD Opteron™ processor vs. 6.4 GT/s for AMD Opteron™ 6100

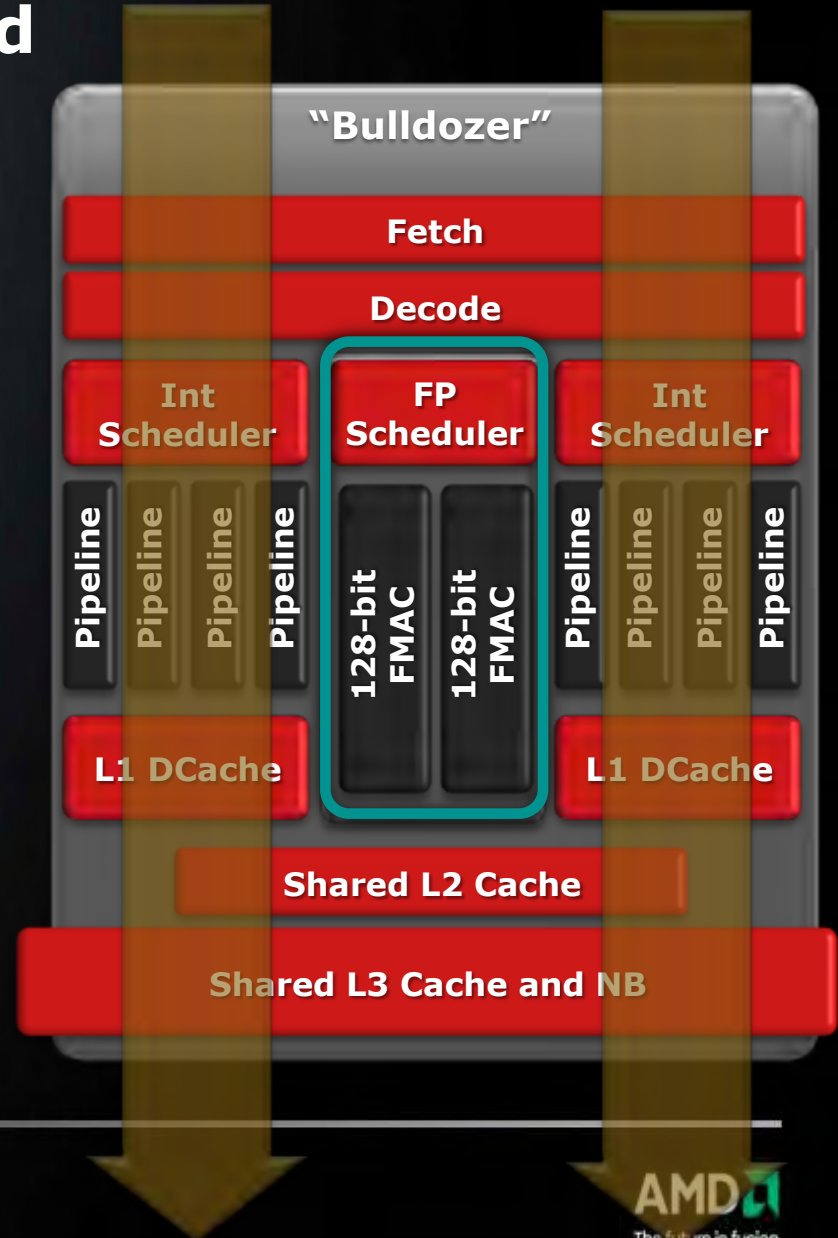
Series processor

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“Bulldozer” x86 Architecture: AMD’s Latest Leap Forward

- Two tightly linked cores share resources to increase efficiency
 - A dedicated integer core for each parallel thread
 - Flexible floating point unit that can be dedicated or shared
- ISA extensions, including FP “FMAC”
- Extensive new power mgmt innovations
- 32nm SOI with high-K metal gate
- 2011 / desktop and server

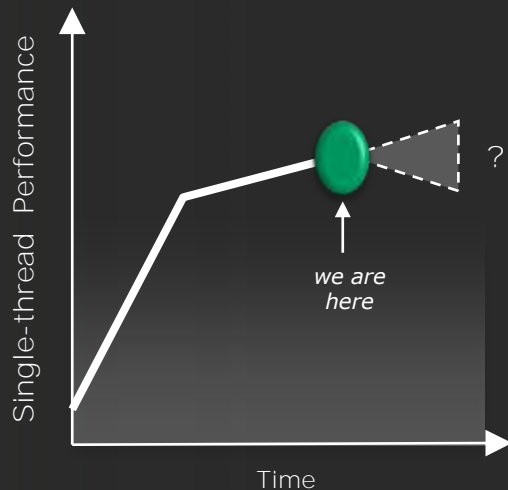


A New Era of Processor Performance

Single-Core Era

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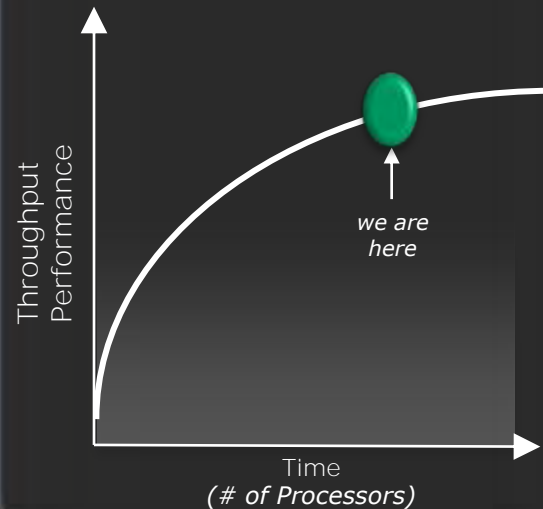
- ✗ Power
- ✗ Complexity



Multi-Core Era

Constrained by:

- ✗ Power
- ✗ Parallel SW availability
- ✗ Scalability



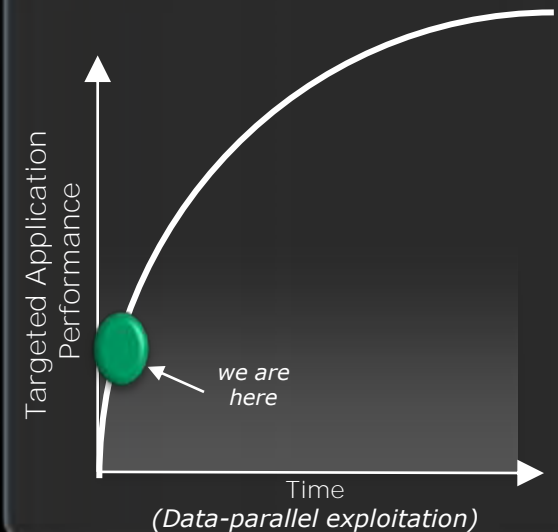
Heterogeneous Systems Era

Enabled by:

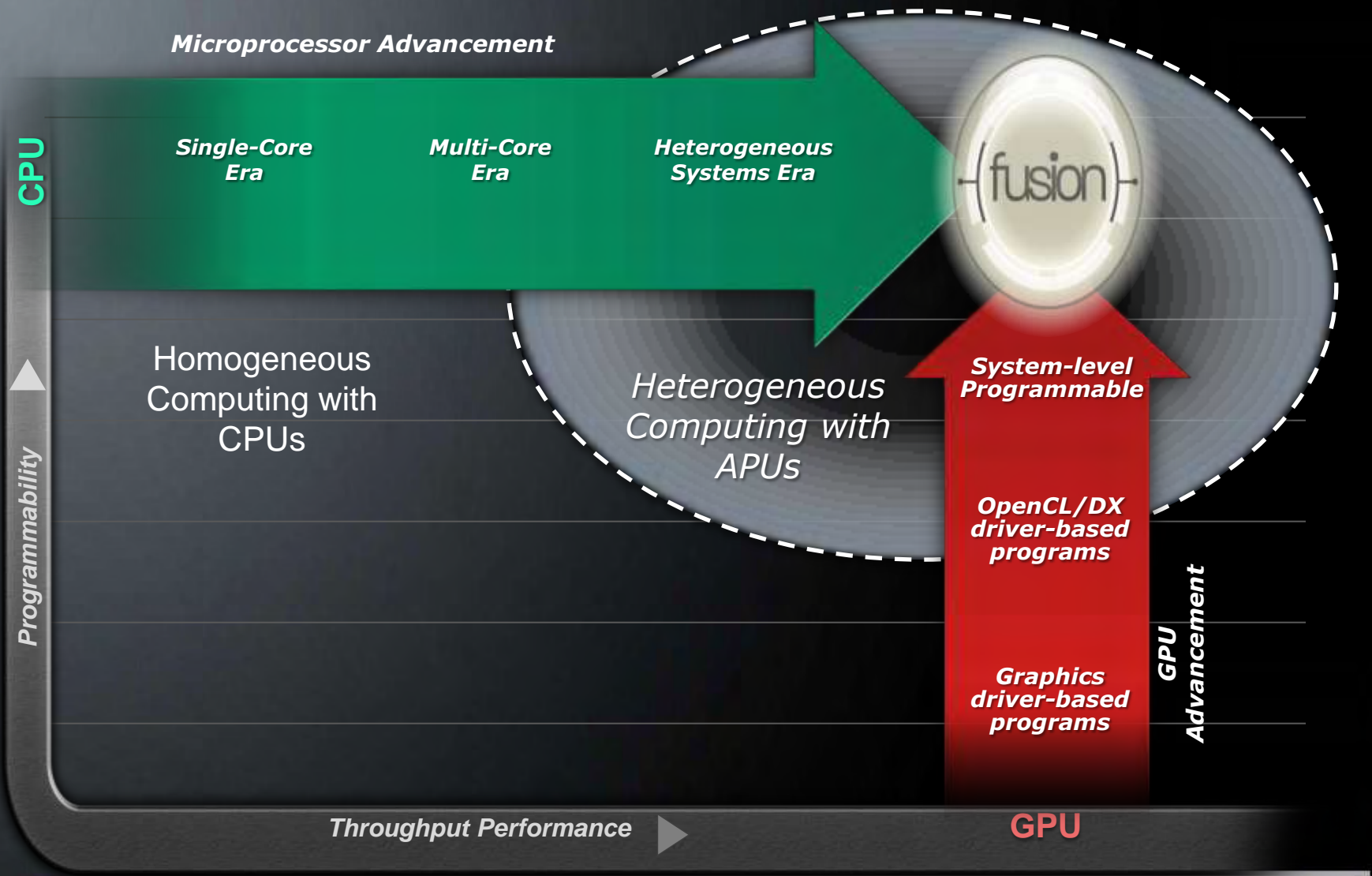
- ✓ Abundant data parallelism
- ✓ Power efficient GPUs

Constrained by:

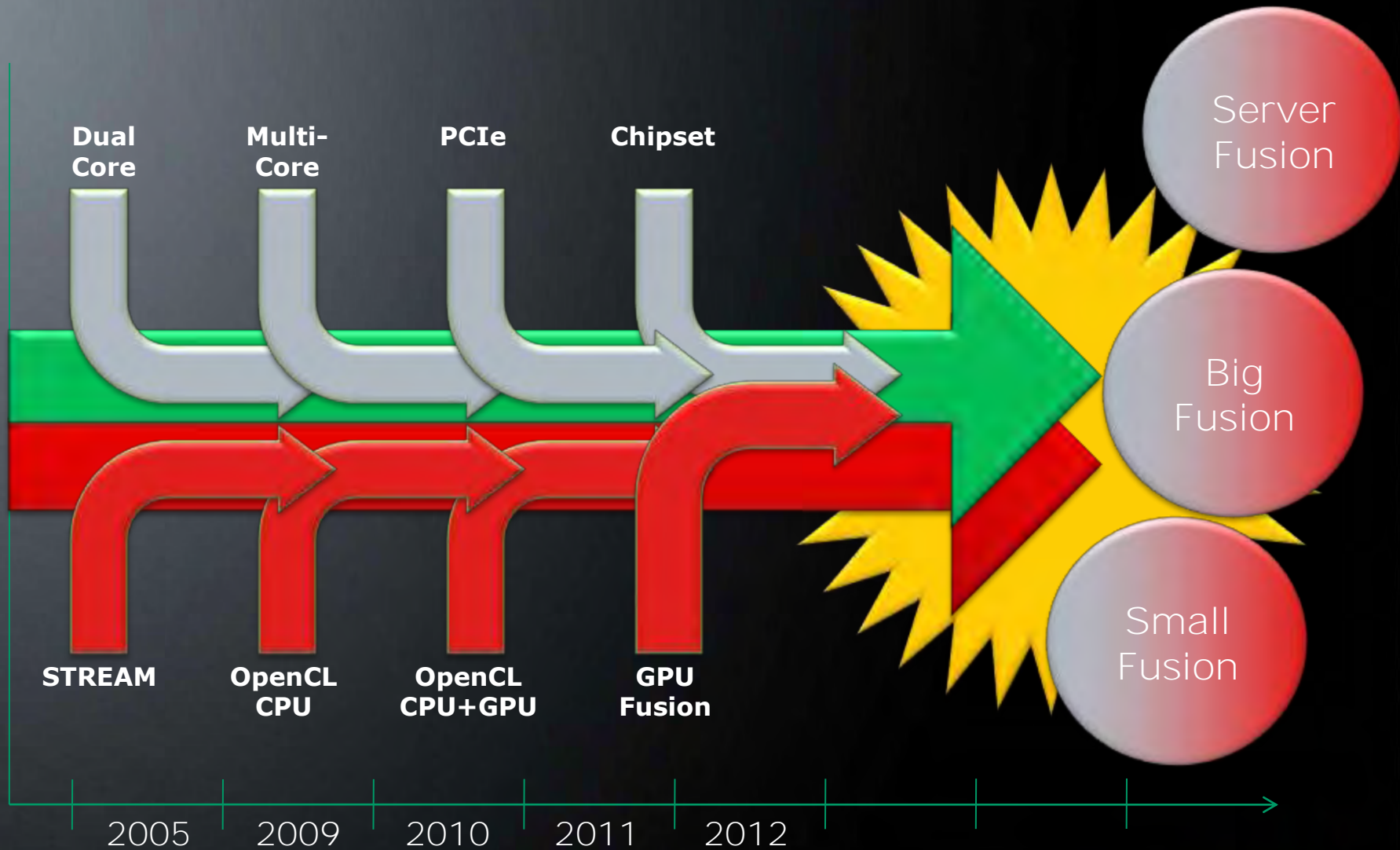
- ✗ Programming models
- ✗ Communication Overheads



AMD Fusion™ APUs: Delivering Heterogeneous Computing



The Future For Servers Is Fusion



Thank You!



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Software Seminar – Late April 2010 (West Houston area)

Converting CUDA code to industry-standard OpenCL

Dr. Udeepa Bordoloi will present a technical comparison of CUDA and OpenCL, highlighting similarities and differences along with hints on how best to convert common CUDA constructs into OpenCL. Then the team will walk through conversion of one or more code examples taken from among algorithms frequently used in Oil & Gas industry applications. After a technical Q&A session, Dr. Bordoloi will be available to discuss and provide guidance on individual CUDA conversion projects.

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